

Figure 1  
(PRIOR ART)

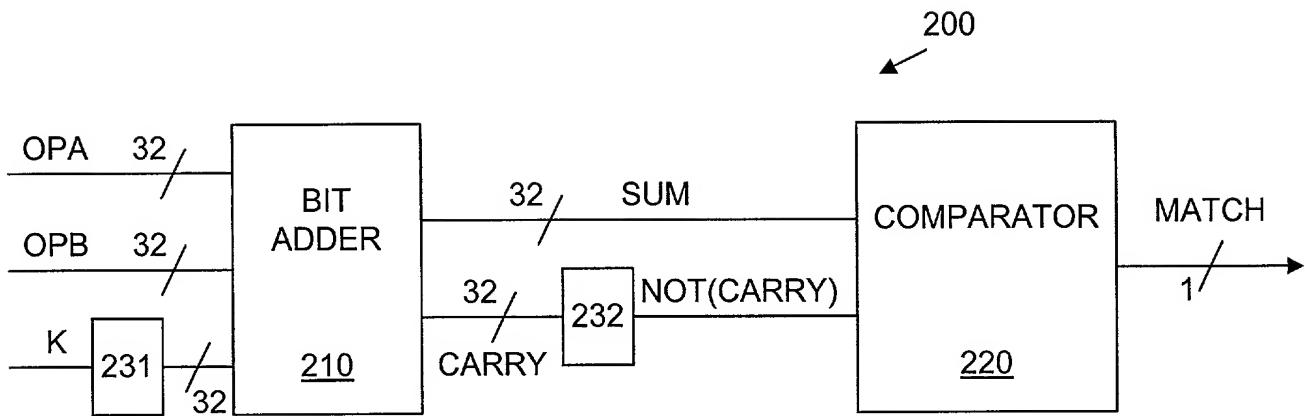


Figure 2A

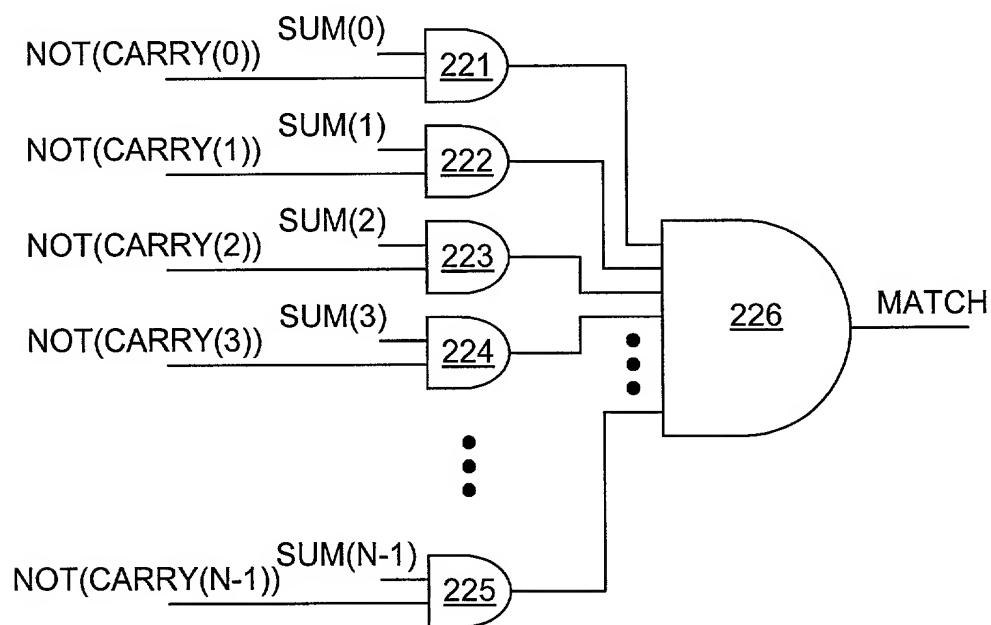


Figure 2B

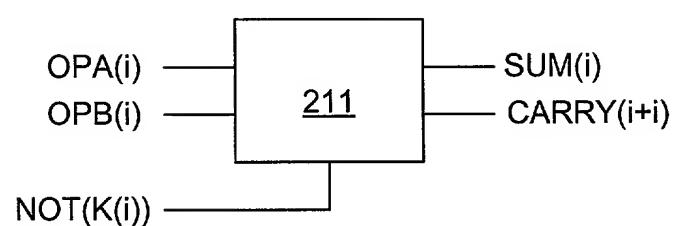


Figure 2C

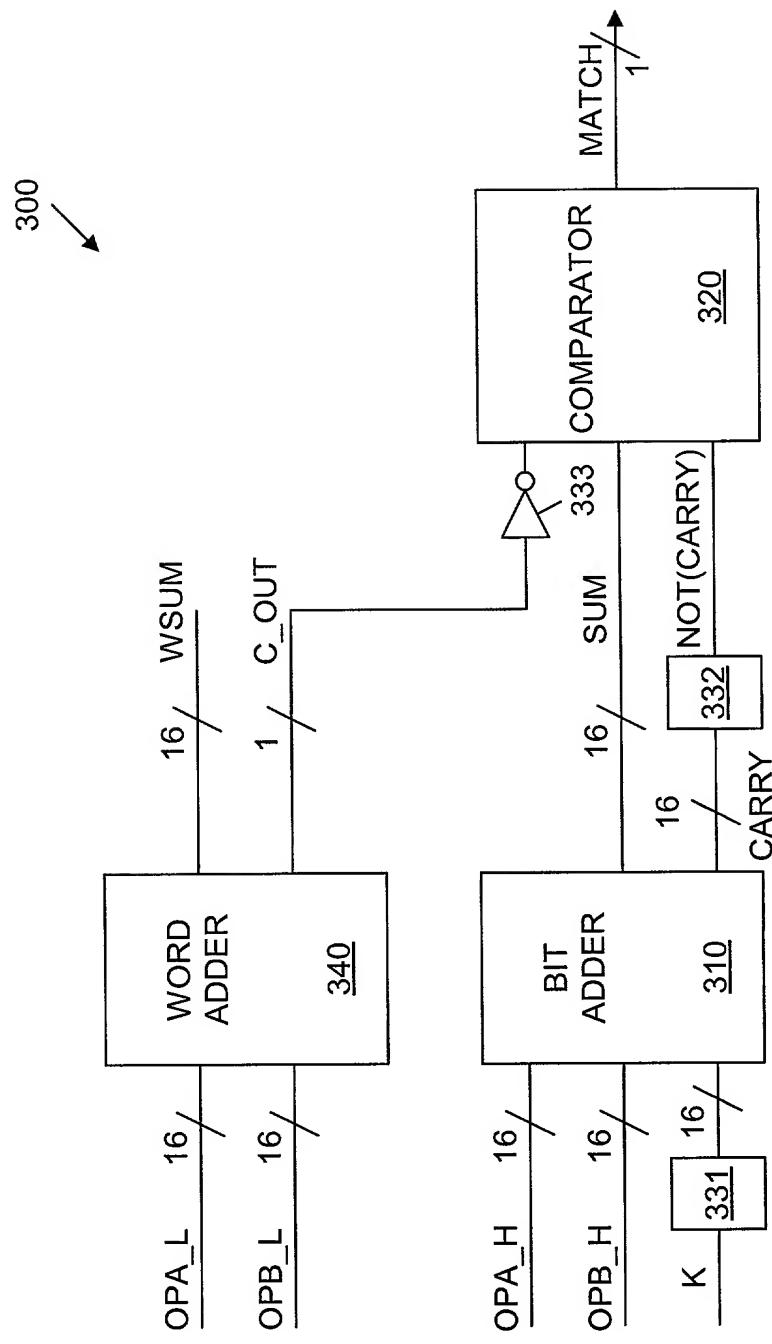


Figure 3

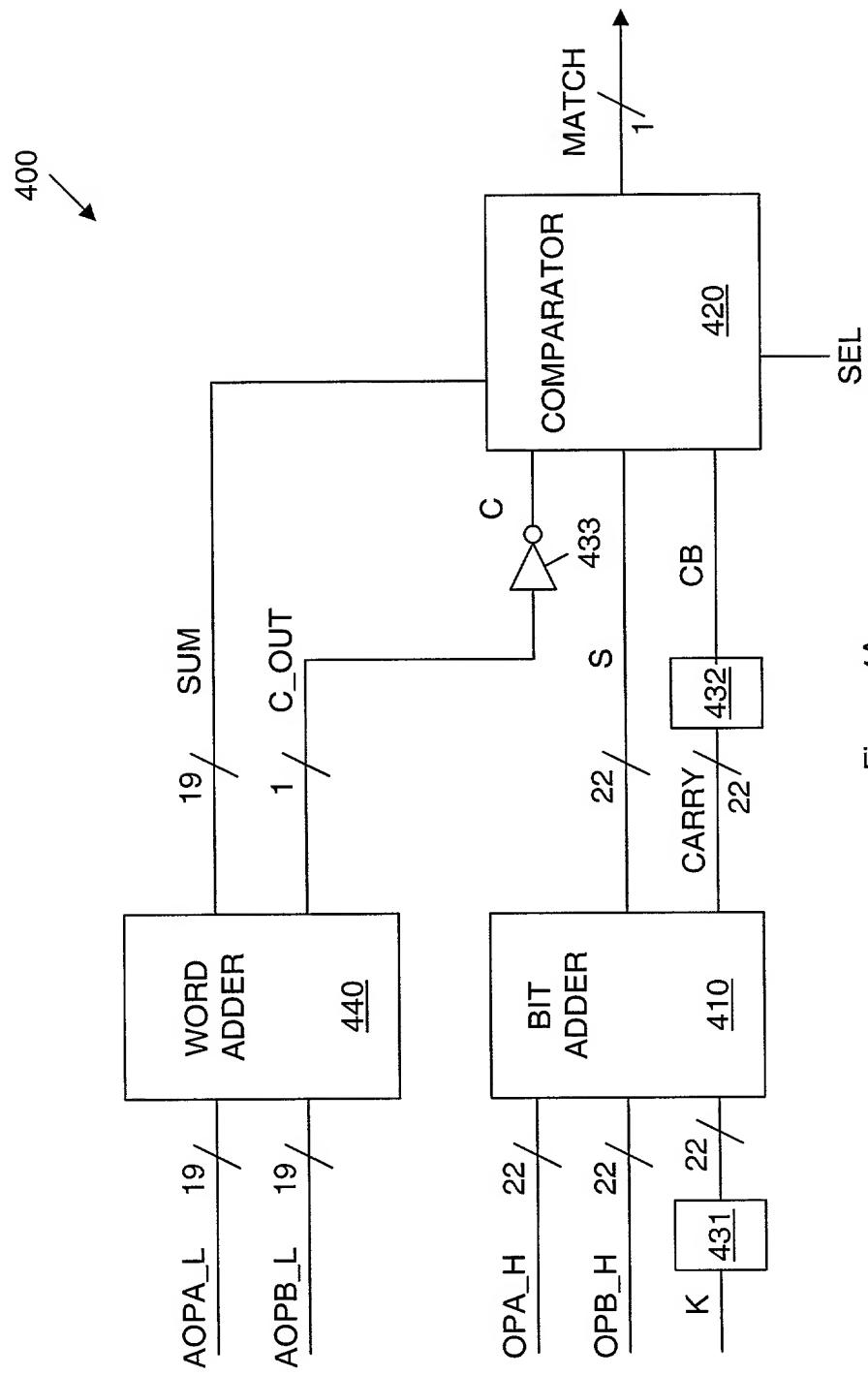


Figure 4A

## "Bit" Register Diagram

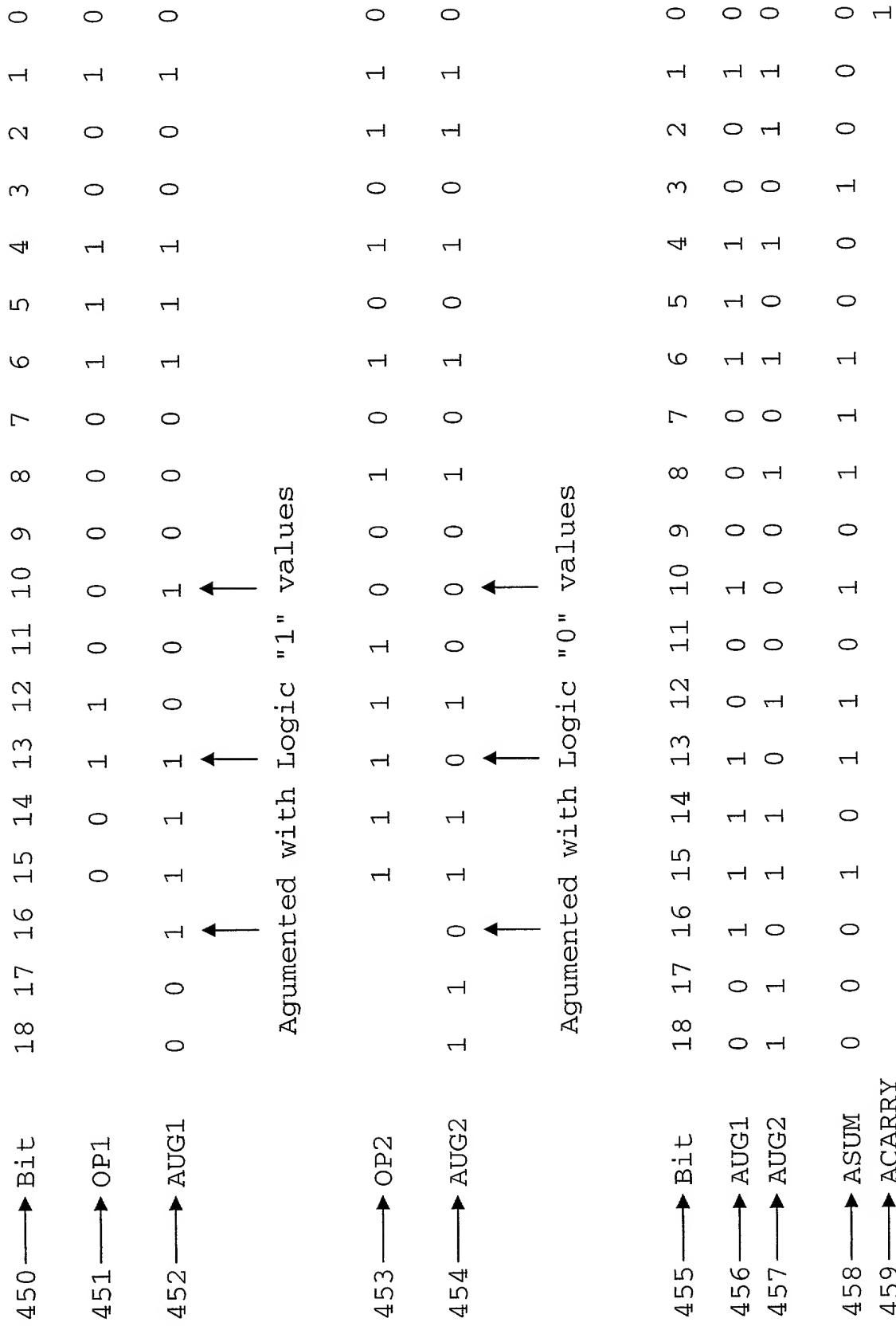


Figure 4B

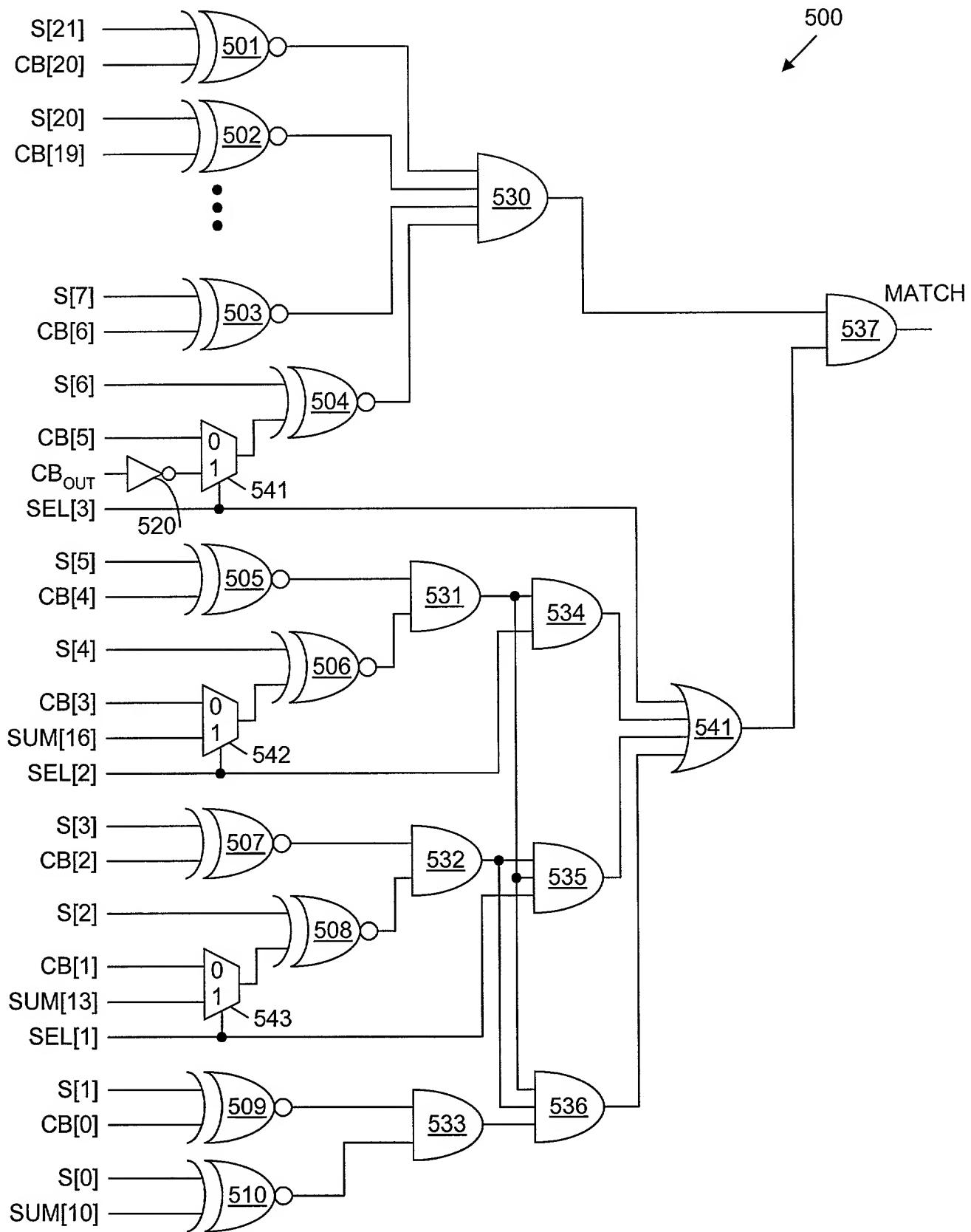


Figure 5